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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

1145
10/08/1479
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APPL NUM 10081479	FILING DATE 02/21/2002	CLASS 430	SUBCLASS 311	GAU 1756	EXAMINER Chacko-Davis
**APPLICANTS: Chiang Min-Hsiung; Lo Chi-Hsin;					
**CONTINUING DATA VERIFIED: <i>none</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>none</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>DL</i>				67,200-576	
TITLE : Method for integrating copper process and MIM capacitor for embedded DRAM					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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